

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 4846**  
Masato MORI et al. : Attorney Docket No. 2006\_1203A  
Serial No. 10/587,280 : Group Art Unit 2892  
Filed July 25, 2006 : Examiner Ajay Arora

CIRCUIT BOARD AND METHOD FOR  
MOUNTING CHIP COMPONENT

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**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement of October 31, 2008, Applicants hereby elect Group I, which is drawn to a substrate including chip components and is embodied by claims 1-5 and 13-16. Accordingly, since Group I has been elected, Applicants further elect Embodiment 1, which is illustrated in Figure 1. Note, that claims 1-12 read on the elected species.

Having made the required election, a full examination on the merits of the elected group is requested.

Respectfully submitted,

Masato MORI et al.

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